I hereby certify that this correspondence is being electronically filed with the United States Patent and Trademark Office.

Attorney for Applicants

Date: 09 January 2009

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/766,611

Applicants : SWANSON, Travis et al.

Filed: 28 January 2004

Title : Providing Memory Test Patterns for DLL Calibration

Art Unit : 2117

Examiner : CHUNG, Phung M.

Atty. Docket No. : PAT001096-000 (013721-0023-999)

TRANSMITTAL LETTER

Mail Stop Petition

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-captioned application are the following:

- ◆ Petition Under 37 CFR 1.182 (2 pp.);
- ♦ Supplemental Declaration (PTO/SB/04) (3 pp.);
- ♦ Application Data Sheet (3 pp.); and
- Exhibit A (page 4 of original Invention Disclosure Form) (1 p.).

The Commissioner is hereby authorized to charge any fees due in connection with this filing to our Deposit Account No. 50-3013.

Respectfully submitted,

Edward L. Pencoske Reg. No. 29,688

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Dated: 09 January 2009